501.39932X00

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	J. NOGUCHI, et al.
Application No.:	09/825,946
Filed:	April 5, 2001
For:	SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND FABRICATION METHOD FOR SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE
Art Group:	2811
Examiner:	H. Vu

INFORMATION DISCLOSURE STATEMENT
UNDER 37 CFR §1.97 & §1.98

Assistant Commissioner for Patents   March 26, 2003
Washington, D.C. 20231

Sir:

In the matter of the above-identified application, applicants submit a copy of the documents listed in the attached form equivalent to Form PTO-1449 for the Examiner's consideration.

This information disclosure statement is being submitted before the mailing date of a notice of allowance and is accompanied by the following certification specified in 37 C.F.R. §1.97(e)(2).

On information and belief, I hereby certify that no item of information contained in the information disclosure statement was cited in a communication from a foreign patent office in a counterpart foreign application, and, to the knowledge of the person signing the certification after making reasonable inquiry, no item of information contained in the information disclosure statement was known to any individual designated in § 1.56(c) more than three months prior to the filing of the information disclosure statement.
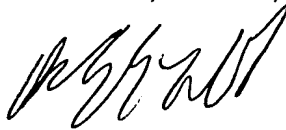
Each of the documents listed on the attached form equivalent to Form PTO-1449 is in the English language.

It is respectfully requested that this information disclosure statement be considered by the Examiner.

Please charge any shortage in the fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account No. 01-2135 (501.39932X00) and please credit any excess fees to such deposit account.

Respectfully submitted,

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